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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/767,376	01/29/2004	Nelson Diaz	16274.169	4765
22913 7590 02/23/2007 WORKMAN NYDEGGER (F/K/A WORKMAN NYDEGGER & SEELEY) 60 EAST SOUTH TEMPLE 1000 EAGLE GATE TOWER SALT LAKE CITY, UT 84111			EXAMINER KIM, DAVID S	
			ART UNIT 2613	PAPER NUMBER
SHORTENED STATUTORY PERIOD OF RESPONSE		MAIL DATE	DELIVERY MODE	
3 MONTHS		02/23/2007	PAPER	

**Please find below and/or attached an Office communication concerning this application or proceeding.**

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

## Office Action Summary

Application No.

10/767,376

Applicant(s)

DIAZ, NELSON

Examiner

David S. Kim

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 29 January 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 29 January 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)            | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948)    | Paper No(s)/Mail Date. _____                                      |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____  | 6) <input type="checkbox"/> Other: _____                          |

## DETAILED ACTION

### Drawings

1. The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the following features must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

In claims 14 and 19-20, these claims include the limitation of "more steps than the initial digital range", but the drawings do not show this limitation. Figs. 3A-3B only show the same amount of steps.

2. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

### Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

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4. **Claims 1-20** are rejected under 35 U.S.C. 103(a) as being unpatentable over Kunst et al. (U.S. Patent No. 6,934,740 B1, hereinafter "Kunst") in view of Knudsen (U.S. Patent No. 6,373,423 B1, hereinafter "Knudsen").

**Regarding claim 1**, Kunst discloses:

A receiver in a fiber optic system configured to receive an optical signal of varying light intensity and to produce a data output signal proportional thereto, the receiver comprising:

an optical detector (106 in Fig. 1) configured to receive the optical signal, the optical detector having a dynamic range of sensitivity between a high optical intensity value and a low optical intensity value (photodiodes operate in limited ranges), the optical detector further configured to convert the received optical signal into an analog electrical signal proportional to the optical intensity of the optical signal ("proportional" in col. 1, l. 24-28);

an electronic circuit (e.g., 112 in Fig. 1) coupled to the optical detector, the electronic circuit configured to receive the analog electrical signal from the optical detector and to produce digital signals representative of the optical intensity of the optical signal (notice the analog to digital conversion of A/D 112) such that the electronic circuit is configured to have an original maximum digital value proportional to the high optical intensity value and an original minimum digital value proportional to the low optical intensity value ("proportional" in col. 1, l. 24-28) thereby defining an original receiver resolution between the original minimum and maximum digital values (e.g., resolution of 14 or 7 bits).

Kunst does not expressly disclose:

an adjustment circuit coupled to the electronic circuit configured to allow the original maximum digital value to be adjusted to an adjusted maximum digital value and to allow the original minimum digital value to be adjusted to an adjusted minimum digital value thereby defining an adjusted receiver resolution between the adjusted minimum and maximum digital values.

However, such adjustment circuits are known in the art. Knudsen shows one example of such a circuit (e.g., analog-to-digital converter (A/D) in Fig. 2B). At the time the invention was made, it would have been obvious to one of ordinary skill in the art to implement the A/D converter teachings of Knudsen

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in the A/D converter 112 of Kunst. One of ordinary skill in the art would have been motivated to do this for various benefits: reduced chip space and power requirements, reduced noise, reduced capacitance problems (Knudsen, col. 2, l. 59-61) and greater resolution (Knudsen, col. 8, l. 24-25).

**Regarding claim 2,** Kunst in view of Knudsen discloses:

The receiver of claim 1 wherein the adjusted maximum digital value is different than the original maximum digital value (Knudsen, sliding window in Fig. 3B).

**Regarding claim 3,** Kunst in view of Knudsen discloses:

The receiver of claim 1 wherein the adjusted minimum digital value is different than the original minimum digital value (Knudsen, sliding window in Fig. 3B).

**Regarding claim 4,** Kunst in view of Knudsen discloses:

The receiver of claim 1 wherein the adjusted maximum digital value is lower than the original maximum digital value and the adjusted minimum digital value is higher than the original minimum digital value (Knudsen, sliding window in Fig. 3B) such that the adjusted receiver resolution is finer than the original receiver resolution (Knudsen, col. 8, l. 24-25).

**Regarding claim 5,** Kunst in view of Knudsen discloses:

The receiver of claim 1 wherein the adjusted maximum digital value is proportional to a highest anticipated optical value for the optical signal received by the optical detector and wherein the adjusted minimum digital value is proportional to a lowest anticipated optical value of the optical signal received by the optical detector (the output is proportional in col. 1, l. 16-28 of Kunst and the range values of Knudsen would be set to fit this output, so the range values would be proportional).

**Regarding claim 6,** Kunst in view of Knudsen discloses:

The receiver of claim 1 wherein the adjusted maximum digital value is less than the original maximum digital value and is proportional to a highest anticipated optical value for the optical signal received by the optical detector and wherein the adjusted minimum digital value is higher than the original minimum digital value is proportional to a lowest anticipated optical value of the optical signal received by the optical detector (Knudsen, sliding window in Fig. 3B; the output is proportional in col. 1, l.

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16-28 of Kunst and the range values of Knudsen would be set to fit this output, so the range values would be proportional).

**Regarding claim 7**, Kunst in view of Knudsen discloses:

The receiver of claim 1 wherein the dynamic range of sensitivity is between a high optical intensity value of positive 7 dBm and a low optical intensity value of negative 20 dBm (Kunst, corresponding range in Watts in col. 1, l. 29-32).

**Regarding claim 8**, Kunst in view of Knudsen discloses:

The receiver of claim 1 wherein the electronic circuit includes an analog-to-digital converter (Kunst, A/D 112 in Fig. 1) configured to receive the analog electrical signal and to convert the electrical signal into digital signals.

**Regarding claim 9**, Kunst in view of Knudsen discloses:

The receiver of claim 8 wherein the analog-to-digital converter converts the analog electrical signal into a series of 8-bit digital values (Knudsen, notice the variable number of bits in col. 8, l. 30-32).

**Regarding claim 10**, Kunst in view of Knudsen discloses:

The receiver of claim 9 wherein the lowest 8-bit digital value is originally the original minimum digital value (Knudsen, Fig. 3B, the lowest value could correspond to ground 112) and then adjusted to the adjusted minimum digital value (Knudsen, Fig. 3B, the sliding lower bound 212B), and wherein the highest 8-bit digital value is originally the original maximum digital value (Knudsen, Fig. 3B, the lowest value could correspond to maximum reference voltage 380B) and then adjusted to the adjusted maximum digital value (Knudsen, Fig. 3B, the sliding upper bound 212A).

**Regarding claim 11**, Kunst in view of Knudsen does not expressly disclose:

The receiver of claim 1 assembled into a intelligent small form factor pluggable module for use with a fiber optic system.

However, Examiner takes Official Notice that such form factor pluggable modules are known in the art. At the time the invention was made, it would have been obvious to one of ordinary skill in the art to assemble the receiver of Kunst in view of Knudsen into one of these modules. One of ordinary skill in the art would have been motivated to do this for at least the benefit of compact size.

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**Regarding claim 12**, Kunst in view of Knudsen discloses:

A fiber optic communication system comprising:

a signal transmitter (Kunst, Fig. 1, implied source of transmitted signals into fiber 102) that produces optical signals of varying light intensity;

an optical fiber (Kunst, Fig. 1, fiber 102) coupled to the signal transmitter that receives and transmits the optical signals;

a receiver (Kunst, Fig. 1, receiving end) coupled to the optical fiber that receives the optical signals and produces a data signal proportional thereto, the receiver further comprising:

an optical detector (Kunst, 106) configured to receive the optical signals, the optical detector having a dynamic range of sensitivity between a high optical value and a low optical value (photodiodes operate in limited ranges), the optical detector further configured to convert the received optical signals into electrical signals proportional to the optical intensity of the optical signals (Kunst, "proportional" in col. 1, l. 24-28);

an electronic circuit (Kunst, 112) coupled to the optical detector, the electronic circuit configured to receive the electrical signals from the optical detector and to have an initial digital range (Kunst, notice the analog to digital conversion of A/D 112) representative of the dynamic range, the initial digital range being defined between an initial maximum digital value and an initial minimum digital value (Kunst, "range" in col. 1, l. 36-42), the initial maximum digital value being proportional to high optical value and the initial minimum digital value being proportional to low optical value ("proportional" in col. 1, l. 24-28); and

an adjustment circuit (A/D 112 of Kunst in view of the A/D teachings of Knudsen, see the treatment of claim 1 above) coupled to the electronic circuit configured to allow the initial maximum digital value to be adjusted to an adjusted maximum digital value and to allow the initial minimum digital value to be adjusted to an adjusted minimum digital value thereby defining an adjusted digital range (Knudsen, sliding window in Fig. 3B), the adjusted maximum digital value being proportional to a highest anticipated optical value and the adjusted minimum digital value being proportional to a lowest anticipated optical value (the output is proportional in

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col. 1, l. 16-28 of Kunst and the range values of Knudsen would be set to fit this output, so the range values would be proportional).

**Regarding claim 13**, Kunst in view of Knudsen discloses:

The fiber optic communication system of claim 12 wherein the adjusted maximum digital value is different than the initial maximum digital value (Knudsen, sliding window in Fig. 3B).

**Regarding claim 14**, Kunst in view of Knudsen discloses:

The fiber optic communication system of claim 12 wherein the adjusted maximum digital value is lower than the initial maximum digital value and the adjusted minimum digital value is higher than the initial minimum digital value (Knudsen, sliding window in Fig. 3B).

Kunst in view of Knudsen does not expressly disclose:

such that the adjusted digital range has more steps than the initial digital range.

However, the addition of more steps is known in the art. At the time the invention was made, it would have been obvious to one of ordinary skill in the art to provide these additional steps. One of ordinary skill in the art would have been motivated to do this to provide the commonly known benefit of increased resolution.

**Regarding claim 15**, Kunst in view of Knudsen discloses:

The fiber optic communication system of claim 12 wherein the electronic circuit includes an analog-to-digital converter (Kunst, A/D 112 in Fig. 1) configured to receive the analog electrical signal and to convert the electrical signal into a digital signal.

**Regarding claim 16**, Kunst in view of Knudsen does not expressly disclose:

The fiber optic communication system of claim 12 wherein the receiver is assembled into an intelligent small form factor pluggable module for use in the fiber optic system.

However, Examiner takes Official Notice that such form factor pluggable modules are known in the art. At the time the invention was made, it would have been obvious to one of ordinary skill in the art to assemble the receiver of Kunst in view of Knudsen into one of these modules. One of ordinary skill in the art would have been motivated to do this for at least the benefit of compact size.



**Regarding claim 17**, Kunst in view of Knudsen discloses:

A receiver in a fiber optic system, the receiver comprising:

an optical detector (Kunst, 106) configured to receive an optical signal of varying light intensity, the optical detector having a dynamic range of sensitivity between a high optical intensity value and a low optical intensity value (photodiodes operate in limited ranges), the optical detector further configured to convert the received optical signal into an analog electrical signal proportional to the optical intensity of the optical signal (Kunst, "proportional" in col. 1, l. 24-28);

an electronic circuit (Kunst, 112) coupled to the optical detector, the electronic circuit configured to receive the analog electrical signal from the optical detector and to produce digital signals (Kunst, notice the analog to digital conversion of A/D 112) representative of the optical intensity of the optical signal such that the electronic circuit is configured with an original maximum digital value proportional to the high optical intensity value and an original minimum digital value proportional to the low optical intensity value (Kunst, "proportional" in col. 1, l. 24-28) thereby defining an original receiver resolution between the original minimum and maximum digital values (Kunst, e.g., resolution of 14 or 7 bits); and

adjustment means (A/D 112 of Kunst in view of the A/D teachings of Knudsen, see the treatment of claim 1 above) coupled to the electronic circuit for adjusting the original maximum digital value to an adjusted maximum digital value and for adjusting the original minimum digital value to an adjusted minimum digital value thereby defining an adjusted receiver resolution (Knudsen, sliding window in Fig. 3B) between the adjusted minimum and maximum digital values.

**Regarding claim 18**, Kunst in view of Knudsen discloses:

A method of adjusting the resolution of a receiver in a fiber optic system, the method including the steps of:

providing an optical detector (Kunst, 106) with a dynamic range sensitivity between a highest optical value and a lowest optical value;

providing an initial digital range (Knudsen, Fig. 3B) representative of the dynamic range, the initial digital range being defined between an initial maximum digital value and an initial minimum digital value, the maximum digital value being proportional to highest optical value and the minimum

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digital value being proportional to lowest optical value (the output is proportional in col. 1, l. 16-28 of Kunst and the range values of Knudsen would be set to fit this output, so the range values would be proportional);

determining an actual optical range for a fiber optic system application, the actual optical range defined between a highest actual optical value and a lowest actual value (e.g., Kunst, corresponding range in Watts in col. 1, l. 29-32); and

adjusting the initial digital range to an adjusted dynamic range, the adjusted digital range being defined between an adjusted maximum digital value and an adjusted minimum digital value, the adjusted maximum digital value being proportional to highest actual optical value and the adjusted minimum digital value being proportional to lowest actual optical value (Knudsen, sliding window in Fig. 3B; the output is proportional in col. 1, l. 16-28 of Kunst and the range values of Knudsen would be set to fit this output, so the range values would be proportional).

**Regarding claim 19**, Kunst in view of Knudsen discloses:

The method of claim 18 wherein the step of adjusting the initial digital range to an adjusted dynamic range includes adjusting the maximum digital value to be lower than the initial maximum digital value (Knudsen, sliding window in Fig. 3B).

Kunst in view of Knudsen does not expressly disclose:

such that the adjusted digital range has more steps than the initial digital range.

However, the addition of more steps is known in the art. At the time the invention was made, it would have been obvious to one of ordinary skill in the art to provide these additional steps. One of ordinary skill in the art would have been motivated to do this to provide the commonly known benefit of increased resolution.

**Regarding claim 20**, Kunst in view of Knudsen discloses:

The method of claim 18 wherein the step of adjusting the initial digital range to an adjusted dynamic range includes adjusting the minimum digital value to be higher than the initial minimum digital value (Knudsen, sliding window in Fig. 3B).

Kunst in view of Knudsen does not expressly disclose:

such that the adjusted digital range has more steps than the initial digital range.

However, the addition of more steps is known in the art. At the time the invention was made, it would have been obvious to one of ordinary skill in the art to provide these additional steps. One of ordinary skill in the art would have been motivated to do this to provide the commonly known benefit of increased resolution.

### **Conclusion**

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Lyons et al. is cited to show a fiber optic system with an optical receiver that employs analog to digital conversion to produce output data signals (e.g., Fig. 1). Chu et al. is cited to show a receiver in an optical system with a high dynamic range that employs analog to digital conversion (abstract). Hutchinson et al. is cited to show an optical receiver with analog to digital conversion that has an automatically and dynamically variable resolution range (abstract). Morizio et al. is cited to show an analog to digital converter with a programmable dynamic range. Ye et al. is cited to show a receiver in an optical system that employs analog to digital conversion with more than one level of resolution (e.g., Fig. 5). Yoshida et al. is cited to show the related issue of receiving optical signals with different levels (col. 2, l. 12-25) and to show an optical receiver with analog to digital conversion (e.g., Fig. 2). Choi et al. is cited to show another optical detector with an adjustable range (e.g., Fig. 6). Tajima et al. is cited to show the related issue of receiving optical signals with different levels (col. 1, l. 21-31) and to show an optical receiver with analog to digital conversion (Fig. 6).

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to David S. Kim whose telephone number is 571-272-3033. The examiner can normally be reached on Mon.-Fri. 9 AM to 5 PM (EST).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kenneth N. Vanderpuye can be reached on 571-272-3078. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

DSK



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SUPERVISORY PATENT EXAMINER